DykeShafferProjects.txt (ver 10/9/20 12/14/20) leadership / ownership projects

===(esaero san luis obispo 2019 thru present)===
systems integration NASA X57 electric aircraft arch / design / implementation
electronic speed control for surveillance drone light aircraft design / implementation

===(sk2x sunnyvale 2018 thru 2019)===
electronic ink product arch / design / implementation / verification

===(i-blades sunnyvale 2016 thru 2018)===
firmware design / implementation / verification
rechargeable battery arch / design / implementation / verification
mobile hardware arch / design / implementation / verification

===(sayhello san francisco 2014 thru 2016)===
firmware design / implementation / verification
energy harvesting arch / design / implementation / verification
wifi/bluetooth hardware arch / design / implementation / verification

===(sandisk sunnyvale 2013 thru 2014)===
wireless power product investigation / design / implementation

===(panamax petaluma 2012 thru 2012)===
zigbee rf soc design / implementation / cost reduction

===(palm sunnyvale 2009 thru 2011)===
hardware application arch / implementation / verification
firmware design / implementation / verification
magnetic design / implementation / verification
bicmos IC design arch / design / implementation / verification
wireless power integration rfq / arch / vendor selection / specification

===(red condor rohnert park 2005 thru 2008)===
redundant data center infrastructure arch / design / implementation
virtual mail exchanger defense arch / design / implementation

===(fairwind farm petaluma 2001 thru 2005)===
designed / built world class dressage training facility

===(fiblerlane/cerent petaluma 1997 thru 2001)===
all three telecom backplanes designed using schematic capture in abstraction
conceived / negotiated / implemented optics integration outsourcing concept
oc48/oc192 line card arch / design / implementation

===(nlc rohnert park 1994 thru 1997)===
telecom backplane / optic line cards using schematic capture in abstraction
optical network unit arch / design / implementation for aerial plant
bidirectional fiber optic in the loop low cost telecom link
optic transmitter dual closed loop power control with back facet monitor diode

===(hp rohnert park 1989 thru 1994)===
schematic capture in abstraction platform independent tools
m68030 controller arch / design / implementation
msib IC technology conversion for modular test measurement
gaas IC wide bandwidth log amp

===(ardent sunnyvale 1988 thru 1989)===
super computer io subsystem arch / design
emi compliance testing / diagnosis / redesign

===(hp santa rosa 1982 thru 1987)===
m68000/020 controller arch / design / implementation
bipolar IC divide by N arch / design / implementation
fractional N 35 to 70 MHz rf synthesizer sub system synthesis spurs

===(hp loveland / fort collins 1976 thru 1982)===
principal in arch / design serialize dual HPIB for modular test measurement
non intrusive cpu real time assembly M68K code hardware development tool
BPC assembler to allow HPL language extension on 9825
language statement extensions to HPL on 9825 desktop computer
bpc hybrid cpu production line diagnostic / testing system
non intrusive cpu real time assembly BPC code hardware development tool
noise reduction balancing shared impedance / shared area coupling
nmos ic analysis error/capacitance/spice extraction from

===(berkleonics 1971 thru 1976)===
household conversion to battery operation using existing ac controls
self tensioning cog wheel gilmer belt drive bicycle push cart
hybrid current/voltage feed back dc-dc converter topology